

EXPRESS MAIL NO. EL872037792US

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Reissue Patent Application:

Applicant : Leland R. Nevill
Assignee: : Micron Technology, Inc.
Filed : November 16, 2001
For : METHOD AND APPARATUS FOR IDENTIFYING INTEGRATED
CIRCUITS
Docket No. : 500060.02

Corresponding Issued U.S. Patent:

Patent No. : 5,984,190
Issued : November 16, 1999
Application No. : 08/857,100
Filing Date : May 15, 1997
Examiner : Daniel St. Cyr
Art Unit : 2876
Date : November 9, 2001

BOX REISSUE

Commissioner of Patents and Trademarks
Washington, DC 20231

FILING FORMAL DRAWINGS IN REISSUE APPLICATION

Sir:

Enclosed are the drawings from the original file of U.S. Patent No. 5,984,190 to be used for the reissue application of U.S. Patent No. 5,984,190. There are no changes in the drawings for the reissue application.

Respectfully submitted,

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Enclosures:

1 Sheet of Drawings, Figures 1-3

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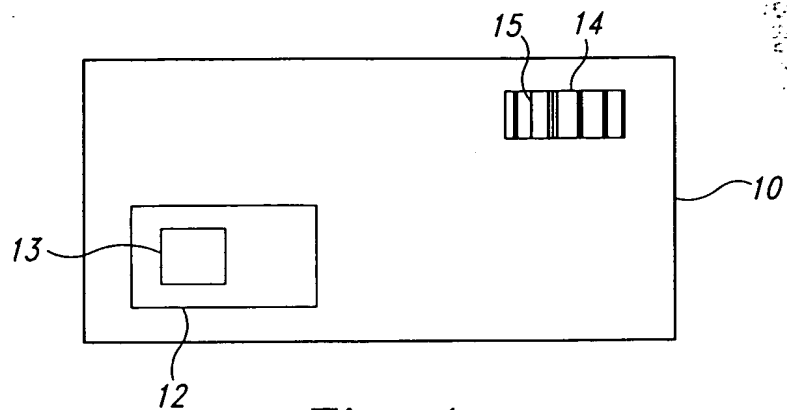


Fig. 1

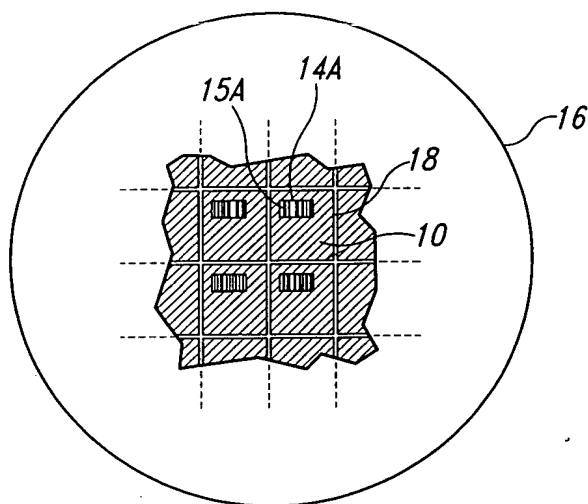


Fig. 2

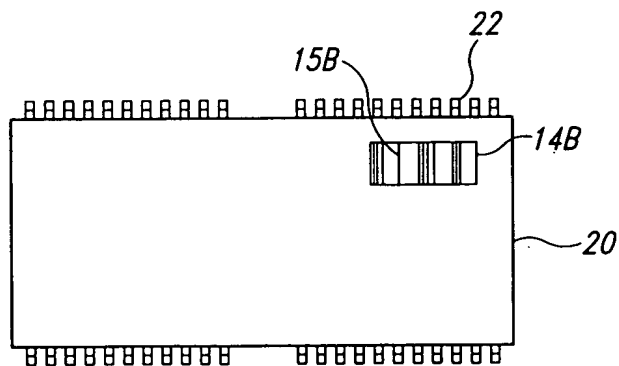


Fig. 3